

```
USE_REVB_PCB = 1 ;Enable to support Rev B motherboard
;This code can be removed when the

USE_CMD_DISP = 1 ;Enable to used CmdLine features
;Disable to use simple character

USE_IM2 = 0 ;Enable if using Interrupt Mode (I
```

```
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;The following compile options should normally only be enabled when everyt
;to run properly. These features provide debugging assistance with NO rel
;-----
```

```
USE_PU_LED_CODES = 0 ;Enable extra LED codes for debugg
USE_OSIF_LED_CODES = 0 ;Enable extra LED codes for debugg
SUPPORT_BREAKPOINTS = 0 ;Enable to compile in Breakpoint s
SHOW_RAM_LOCATION_WITHOUT_CALLS = 0 ;Enable to display XXXXYYYY for RA
;(should only be enabled when noth
```

```
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;The following compile options should normally be enabled, but can be disa
;-----
```

```
SUPPORT_HARD_DISK = 1
```

```
-----
;
; Motherboard built-in ports
;
;-----
```

```
.if USE_REVB_PCB
    DISPPORT = 0x9d ;Video display port address (Write
    SWITCHPORT = 0x9c ;DIP switch port (Read Only)
    LEDPORT = 0x9c ;LED display port (Write Only - '1
.else
    DISPPORT = 0xff ;Video display port address (Write
    SWITCHPORT = 0xff ;DIP switch port (Read Only)
    LEDPORT = 0xfe ;LED display port (Write Only - '0
```

```

.endif

;-----
; Display port related
;-----

DISPCHARS = 64 ;chars/line
DISPLINES = 16 ;lines/display
DISPHOME = 0x7f ;'home' command
DISPWRBIT = 0x80 ;D7 needs to be strobed high
; to be written to the video

; led port related
LED_RESET = ~0x01 ;Reset state
LED_NORAM = ~0x02 ;No RAM found state
LED_RAM = ~0x03 ;RAM found state

.if USE_PU_LED_CODES
LEDIMONITOR = ~0x04 ;About to call InitMonitor
LEDBOOTMSG = ~0x05 ;About to display boot message
LEDDRAMMSG = ~0x06 ;About to display RAM message
LEDDRAMSTART = ~0x07 ;About to display start of RAM
LEDDRAMSPACE = ~0x08 ;About to display space between
LEDDRAMEND = ~0x09 ;About to display end of RAM
LEDDRAMNL = ~0x0a ;About to display new line after
LEDMAINLOOP = ~0x0b ;Power up done, jumping to main

LEDMLINIT = ~0x0c ;Main loop initialization entered
LEDMLDEDONE = ~0x0d ;Main loop initialization - done
LEDMLBFFCNS = ~0x0e ;Main loop about to call main
LEDMLFCNDONE = ~0x0f ;Main loop done processing main

LEDMONI1 = ~0x10 ;Monitor init - about to display
LEDMONI2 = ~0x11 ;Monitor init - initial prompt
LEDMONL1 = ~0x12 ;Monitor loop - about to check
LEDMONL2 = ~0x13 ;Monitor loop - key pressed call
LEDMONL3 = ~0x14 ;Monitor loop - ParseCmds complete
LEDMONL4 = ~0x15 ;Monitor loop - new line and prompt
LEDMONL5 = ~0x16 ;Monitor loop - exiting monitor

LEDCLI1 = ~0x20 ;Command Line Init - about to
LEDCLI2 = ~0x21 ;Command Line Init - about to
LEDCLI3 = ~0x22 ;Command Line Init - complete
LEDCLU1 = ~0x23 ;Command Line DispUpdate - about
LEDCLU2 = ~0x24 ;Command Line DispUpdate - but
LEDCLU3 = ~0x25 ;Command Line DispUpdate - screen
LEDCLU4 = ~0x26 ;Command Line DispUpdate - cursor

```

```
.endif

.if USE_OSIF_LED_CODES
    LEDOS1 = ~0x30           ;OS I/F RST 08 entry
    LEDOS2 = ~0x31           ;OS I/F RST 08 'c' reg parameter
    LEDOS3 = ~0x32           ;OS I/F function invoked - Get
    LEDOS4 = ~0x33           ;OS I/F function invoked - Get
    LEDOS5 = ~0x34           ;OS I/F function invoked - Get
    LEDOS6 = ~0x35           ;OS I/F function invoked - Cle
    LEDOS7 = ~0x36           ;OS I/F function invoked - Hor
    LEDOS8 = ~0x37           ;OS I/F function invoked - Cur
    LEDOS9 = ~0x38           ;OS I/F function invoked - Cur
    LEDOSa = ~0x39           ;OS I/F function invoked - Ena
    LEDOSb = ~0x3a           ;OS I/F function invoked - Dis
    LEDOSc = ~0x3b           ;OS I/F function invoked - Dis
    LEDOSd = ~0x3c           ;OS I/F function invoked - Dis
    LEDOSe = ~0x3d           ;OS I/F function invoked - Nev
    LEDOSf = ~0x3e           ;OS I/F function invoked - Set
.endif

;-----
; switch port related
;-----
    SW_MONITOR_ENABLED = 0x01           ;ROM monitor enabled if switch
    SW_BRKPT_ENABLED = 0x02           ;breakpoint feature enabled if

;-----
; Motherboard expansion ports
;-----

; Expansion Board Base Addresses
Exp0Base = 0x80
Exp1Base = 0x84
Exp2Base = 0x88
Exp3Base = 0x8c
Exp4Base = 0x90
Exp5Base = 0x94
Exp6Base = 0x98           ; NOT POPULATED
MBBase = 0x9c
```

```
NumExpPorts = 6
```

```
;-----  
;  
; Register Configuration  
;  
;-----
```

```
; Expansion Board Register Offsets
```

```
ExpCmdOffset      = 0x00 ; Command register offset
```

```
ExpDataOffset     = 0x01 ; Data register offset
```

```
ExpXOffset        = 0x02 ; to be defined
```

```
ExpIDOffset       = 0x03 ; ID register
```

```
  ;Expansion Feature Codes
```

```
  ExpIDKybd        = 0xa0
```

```
    ;reg ExpXOffset is a 7.983mS free running 8 bit readable counter
```

```
  ExpIDHD          = 0xa1
```

```
    HDCMDRS = 0xa3
```

```
    ;hard disk read sector command
```

```
    ; then 1 sector of data is returned
```

```
    HDCMDWS = 0xa4
```

```
    ;hard disk write sector command
```

```
    ; then 1 sector of data is written
```

```
    HDCMDGS = 0xa6
```

```
    ;hard disk get status command
```

```
      HDCMDGSL = 6
```

```
    ; expect 6 bytes of data
```